



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark S. Moir et al.

Title: SOFTWARE TRANSACTIONAL MEMORY FOR DYNAMICALLY
SIZABLE SHARED DATA STRUCTURES

Application No.: 10/621,072

Filed: July 16, 2003

Examiner: Mano Padmanabhan

Group Art Unit: 2188

Atty. Docket No.: 004-8428

September 27, 2004

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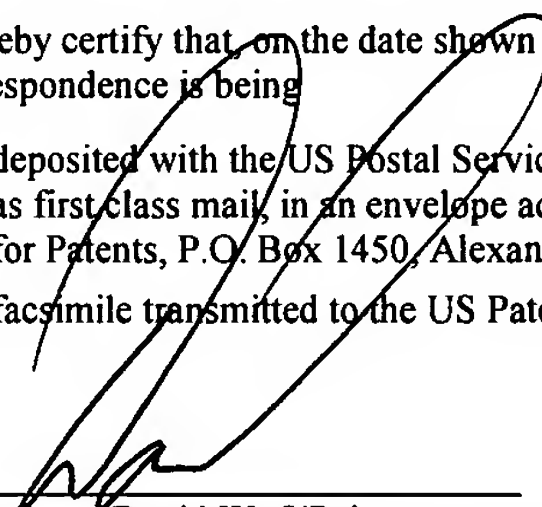
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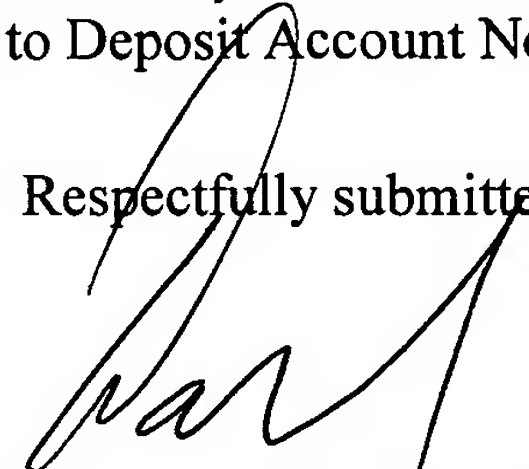
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Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8428
		Application No.: 10/621,072
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s): Moir et al.
(Use several sheets if necessary)		Filing Date: July 16, 2003
		Group Art Unit: 2188
		Date Submitted: September 27, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1	Afek, Yehuda et al., "Long-Lived Renaming Made Adaptive", 18 th Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, "Wait-Free Made Fast", 27 th Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., "DCAS-Based Concurrent Deques", 12 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
	4	Anderson, James H. et al., "Using Local-Spin k-Exclusion Algorithms to Improve Wait-Free Object Implementations", 12 th Annual ACM Symposium on Principles of Distributed Computing, November 1995 (revised 1996, 1997).
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	7	Barnes, Greg, "A Method for Implementing Lock-Free Shared Data Structures", 5 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 261-270, 1993.
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	14	Herlihy, Maurice, "Dynamic-Sized Lockfree Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
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	16	Herlihy, Maurice et al., "The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
Examiner		Date Considered
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

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	17	Herlihy, Maurice et al., "Transactional Memory: Architectural Support for Lock-Free Data Structures", 20 th International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "Obstruction-Free Synchronization: Double-Ended Queues as an Example", 23 rd International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives", 13 th Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lamport, Leslie, "How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs", IEEE Transactions on Computers, September 1979.
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